AMENDMENTS TO THE SPECIFICATION

Please amend the first paragraph 1 on page 1 of the specification as follows:

The present application is related to pending, commonly assigned, and concurrently filed U.S. Patent Application Serial No. 09/680,544 ~~[Attorney Docket No. 10002221-1 (47607-P225US-10013941)]~~ entitled "EMULATION OF DYNAMICALLY RECONFIGURABLE COMPUTER SYSTEM," filed October 4, 2000, which is hereby incorporated herein by reference.

Please amend the paragraph beginning on line 3, page 8 of the specification as follows:

In an exemplary sequence of events, host device 101 may interact with dedicated storage device ~~1 201-1~~ 204-1 and leave data therein, and then operate to retrieve data from another device, such as dedicated storage device 203. Upon regaining access to external dedicated storage device 204-1, the data left therein when the host device 101 was previously in communication therewith, will preferably be preserved unchanged. In this manner, the present invention provides for more realistic emulation than would have been possible employing the prior art "shared storage" where data would be erased by switching from emulation of one storage device to another by an emulator reset operation. In the embodiment of FIGURE 2, emulator 201 enables the host device to successively access a sequence of different emulating storage devices, while each of the emulating storage devices preserves data stored therein between successive emulation sessions with the host.

Please amend the paragraph beginning on line 14, page 8 of the specification as follows:

FIGURE 3 depicts a typical target mode emulator program 300 within a high availability storage system. Host controller 301 is generally a subset of host device 101 depicted in FIGURE 2. In a preferred embodiment, the upper portion of the FIGURE represents components of host computer. The host computer preferably includes host controller 301 that is attached to a host BUS adapter 302 (HBA) which preferably coordinates communication with the emulator 304. The host computer also preferably includes

non-emulated dedicated storage devices 303-1 to 303-N which preferably remain operative and under the control of host controller 301. Generally, the emulation process will involve emulating one of the non-emulated dedicated storage devices 303-1 to 303-N (storage devices native to the host computer system) at a time employing one or more of the non-volatile dedicated emulator storage devices 308-1 to 308-N.

Please amend the paragraph beginning on line 25, page 8 of the specification as follows:

In a preferred embodiment, emulator 304, which preferably includes emulator data interface port 306, is coupled to the controller HBA 302. Within emulator 304, command and control processing 305 preferably coordinates communication between emulator data interface port 306 and internal host bus adaptor (HBA) ports 307-1 to 307-N. Although the embodiment of FIGURE 3 depicts a deployment of one HBA port for each non-volatile dedicated storage device, in an alternative embodiment, a single HBA port may be deployed to interface with all deployed non-volatile emulator storage devices. Such an alternative embodiment 400 is shown in FIGURE 4.